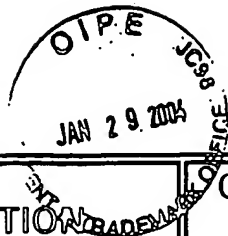


**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).



SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>		<b>CUSTOMER NUMBER  26615</b>	ATTORNEY'S DKT No. H1493		APPLICATION No. 10/674,400		
			APPLICANT(S) Wiley Eugene HILL et al.				
			FILING DATE October 1, 2003		GROUP 2811		
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
<i>MT</i>	Copy of U.S. Serial No. 10/728,844; filed December 8, 2003; entitled: "MERGED FINFET INVERTER/LOGIC GATE"; 51 pages.						
EXAMINER <i>M. Mulholland</i>				DATE CONSIDERED <i>05/20/04</i>			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).